Subject index of volume 5

Ada tasking 159
Adaptive routing 1
Agreement 175
Analysis 145
Array partitioning 133
Asynchronous circuit 107

Broadcasting 73
Byzantine Agreement 219

Clock synchronization 73
Collision detection 67
Communication behaviors 107
Communication protocol 83, 145
Computation complexity 201
Concurrent programming 187
Correctness 187
CSP 83, 95

Deflection routing 1
Delay-insensitive circuit 107
Distributed parallelism 7
Distributed system 159, 175, 219

Emulation 67

Fault tolerance 175, 201, 219
Fault tolerant systems 95
Formal description technique 145
Formal model 145
Formal proof 25
Formal specification 95
Formal verification 95

Garbage collection 187

Hot potato routing 1
Hypercube network 1

Image processing 55
Invent and verify paradigm 95

Linear arrays 55
Link failures 121
Liveness 37
Lower bounds 121, 201

Message complexity 121
Misrouting 1
Modularity 159
Multicasting 73
Multiple access networks 73

Non-determinism 159

OCCAM 37
Optimal algorithms 55

Parallel algorithm 133
Parallel computation 201
Parallelizing compiler 7
Partial correctness 159
Proof system 37
Protocol 25

Radio networks 67
Randomization 25
Regular expressions 107
Ring configuration 121
Ring network 25
Robust algorithms 201

Safety 37
Self-adjusting algorithm 219
Shortest path 133
Specification 145
Specification case study 83
Systolic array 7, 133

Top-down design 95
Trace semantics 107
Transitive closure 133

VLSI algorithm 133

Subject index of volume 5

Ada tasking 159
Adaptive routing 1
Agreement 175
Analysis 145
Array partitioning 133
Asynchronous circuit 107

Broadcasting 73
Byzantine Agreement 219

Clock synchronization 73
Collision detection 67
Communication behaviors 107
Communication protocol 83, 145
Computation complexity 201
Concurrent programming 187
Correctness 187
CSP 83, 95

Deflection routing 1
Delay-insensitive circuit 107
Distributed parallelism 7
Distributed system 159, 175, 219

Emulation 67

Fault tolerance 175, 201, 219
Fault tolerant systems 95
Formal description technique 145
Formal model 145
Formal proof 25
Formal specification 95
Formal verification 95

Garbage collection 187

Hot potato routing 1
Hypercube network 1

Image processing 55
Invent and verify paradigm 95

Linear arrays 55
Link failures 121
Liveness 37
Lower bounds 121, 201

Message complexity 121
Misrouting 1
Modularity 159
Multicasting 73
Multiple access networks 73

Non-determinism 159

OCCAM 37
Optimal algorithms 55

Parallel algorithm 133
Parallel computation 201
Parallelizing compiler 7
Partial correctness 159
Proof system 37
Protocol 25

Radio networks 67
Randomization 25
Regular expressions 107
Ring configuration 121
Ring network 25
Robust algorithms 201

Safety 37
Self-adjusting algorithm 219
Shortest path 133
Specification 145
Specification case study 83
Systolic array 7, 133

Top-down design 95
Trace semantics 107
Transitive closure 133

VLSI algorithm 133